

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-0310US1	Application No. 10/070,091
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant Gilbert Wolrich et al.	
		Filing Date June 28, 2002	Group Art Unit 2183

(37 CFR §1.98(b))

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	A1	2002/0038403	03/2002	Wolrich et al.			
	A2	2007/0234009	10/2007	Wolrich et al.			
	A3	3,577,189	05/1971	Cocke et al.			
	A4	3,881,173	04/1975	Larsen et al.			
	A5	4,454,595	06/1984	Cage			
	A6	4,471,426	09/1984	McDonough			
	A7	4,477,872	10/1984	Losq et al.			
	A8	4,569,016	02/1986	Hao et al.			
	A9	4,606,025	08/1986	Peters et al.			
	A10	4,742,451	05/1988	Bruckert et al.			
	A11	4,754,398	06/1988	Pribnow			
	A12	4,755,966	07/1988	Lee et al.			
	A13	4,808,988	02/1989	Burke et al.			
	A14	4,816,913	03/1989	Harney et al.			
	A15	4,847,755	07/1989	Morrison et al.			
	A16	4,868,735	09/1989	Moller et al.			
	A17	5,045,995	09/1991	Levinthal et al.			
	A18	5,056,015	10/1991	Baldwin et al.			
	A19	5,165,025	11/1992	Lass			
	A20	5,170,484	12/1992	Grondalski			
	A21	5,187,800	02/1993	Sutherland			
	A22	5,189,636	02/1993	Patti et al.			
	A23	5,202,972	04/1993	Gusefski et al.			
	A24	5,220,669	06/1993	Baum et al.			
	A25	5,247,671	09/1993	Adkins et al.			
	A26	5,274,770	12/1993	Khim Yeoh et al.			
	A27	5,394,530	02/1995	Kitta			

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-0310US1	Application No. 10/070,091
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant Gilbert Wolrich et al.	
		Filing Date June 28, 2002	Group Art Unit 2183

(37 CFR §1.98(b))

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	A28	5,428,779	06/1995	Allegrucci et al.			
	A29	5,463,746	10/1995	Brodnax et al.			
	A30	5,509,130	04/1996	Trauben et al.			
	A31	5,541,920	07/1996	Angle et al.			
	A32	5,544,337	08/1996	Beard et al.			
	A33	5,572,690	11/1996	Molnar et al.			
	A34	5,600,848	02/1997	Sproull et al.			
	A35	5,610,864	03/1997	Manning			
	A36	5,640,538	06/1997	Dyer et al.			
	A37	5,669,012	09/1997	Shimizu et al.			
	A38	5,680,564	10/1997	Divivier et al.			
	A39	5,701,435	12/1997	Chi			
	A40	5,704,054	12/1997	Bhattacharya			
	A41	5,721,869	02/1998	Imakawa			
	A42	5,724,563	03/1998	Hasegawa			
	A43	5,748,950	05/1998	White et al.			
	A44	5,787,454	07/1998	Rohlman			
	A45	5,802,373	09/1998	Yates et al.			
	A46	5,812,811	09/1998	Dubey et al.			
	A47	5,815,698	09/1998	Holmann et al.			
	A48	5,815,714	09/1998	Shridhar et al.			
	A49	5,819,080	10/1998	Dutton et al.			
	A50	5,822,619	10/1998	Sidwell			
	A51	5,832,258	11/1998	Kiuchi et al.			
	A52	5,838,975	11/1998	Abramson et al.			
	A53	5,848,276	12/1998	King et al.			
	A54	5,857,104	01/1999	Natarjan et al.			

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-0310US1	Application No. 10/070,091
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant Gilbert Wolrich et al.	
		Filing Date June 28, 2002	Group Art Unit 2183

(37 CFR §1.98(b))

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	A55	5,859,789	01/1999	Sidwell			
	A56	5,859,790	01/1999	Sidwell			
	A57	5,860,085	01/1999	Stormon et al.			
	A58	5,872,963	02/1999	Bitar et al.			
	A59	5,875,355	02/1999	Sidwell et al.			
	A60	5,875,470	02/1999	Drcibelbis et al.			
	A61	5,884,069	03/1999	Sidwell			
	A62	5,898,866	04/1999	Atkins et al.			
	A63	5,923,872	07/1999	Chrysos et al.			
	A64	5,926,646	07/1999	Pickett et al.			
	A65	5,928,358	07/1999	Takayama et al.			
	A66	5,937,177	08/1999	Molnar et al.			
	A67	5,943,491	08/1999	Sutherland et al.			
	A68	5,944,816	08/1999	Dutton et al.			
	A69	5,946,222	08/1999	Redford			
	A70	5,956,514	09/1999	Wen et al.			
	A71	5,993,627	11/1999	Anderson et al.			
	A72	6,005,575	12/1999	Colleran et al.			
	A73	6,052,769	04/2000	Huff et al.			
	A74	6,061,710	05/2000	Eickemeyer et al.			
	A75	6,061,711	05/2000	Song et al.			
	A76	6,100,905	08/2000	Sidwell			
	A77	6,101,599	08/2000	Wright et al.			
	A78	6,115,777	09/2000	Zahir et al.			
	A79	6,139,199	10/2000	Rodriguez			
	A80	6,141,348	10/2000	Muntz			
	A81	6,145,077	11/2000	Sidwell et al.			

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-0310US1	Application No. 10/070,091
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant Gilbert Wolrich et al.	
		Filing Date June 28, 2002	Group Art Unit 2183

(37 CFR §1.98(b))

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	A82	6,145,123	11/2000	Torrey et al.			
	A83	6,157,988	12/2000	Dowling			
	A84	6,195,739	02/2001	Wright et al.			
	A85	6,205,468	03/2001	Diepstraten et al.			
	A86	6,223,208	04/2001	Kiefer et al.			
	A87	6,223,277	04/2001	Karguth			
	A88	6,230,119	05/2001	Mitchell			
	A89	6,230,230	05/2001	Joy et al.			
	A90	6,230,261	05/2001	Henry et al.			
	A91	6,259,699	07/2001	Opalka et al.			
	A92	6,269,391	07/2001	Gillespie			
	A93	6,275,508	08/2001	Aggarwal et al.			
	A94	6,279,066	08/2001	Velingker			
	A95	6,304,956	10/2001	Tran			
	A96	6,314,510	11/2001	Saulsbury et al.			
	A97	6,338,133	01/2002	Schroter			
	A98	6,378,124	04/2002	Bates et al.			
	A99	6,378,125	04/2002	Bates et al.			
	A100	6,385,720	05/2002	Tanaka et al.			
	A101	6,401,155	06/2002	Saville et al.			
	A102	6,442,669	08/2002	Wright et al.			
	A103	6,446,190	09/2002	Barry et al.			
	A104	6,496,847	12/2002	Bugnion et al.			
	A105	6,505,229	01/2003	Turner et al.			
	A106	6,523,108	02/2003	James et al.			
	A107	6,543,049	04/2003	Bates et al.			
	A108	6,560,629	05/2003	Harris			

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-0310US1	Application No. 10/070,091
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant Gilbert Wolrich et al.	
		Filing Date June 28, 2002	Group Art Unit 2183

(37 CFR §1.98(b))

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	A109	6,560,671	05/2003	Samra et al.			
	A110	6,564,316	05/2003	Pcrets et al.			
	A111	6,574,702	06/2003	Khanna et al.			
	A112	6,697,935	02/2004	Borkenhagen et al.			
	A113	6,718,457	04/2004	Tremblay et al.			
	A114	6,784,889	08/2004	Radke			
	A115	6,836,838	12/2004	Wright et al.			
	A116	6,862,676	03/2005	Knapp et al.			
	A117	6,934,951	08/2005	Wilkinson, III et al.			
	A118	6,971,103	11/2005	Hokenek et al.			
	A119	6,976,095	12/2005	Wolrich et al.			
	A120	6,983,350	01/2006	Adiletta et al.			
	A121	7,020,871	03/2006	Bernstein et al.			
	A122	7,181,594	02/2007	Wilkinson, III et al.			
	A123	7,185,224	02/2007	Fredenburg et al.			
	A124	7,191,309	03/2007	Wolrich et al.			
	A125	7,302,549	11/2007	Wilkinson, III et al.			
	A126	7,421,572	09/2008	Wolrich et al.			

**Foreign Patent Documents or Published Foreign Patent Applications**

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	A127	0 130 381	01/1985	Europe				
	A128	0 463 855	01/1992	Europe				
	A129	0 476 628	03/1992	Europe				
	A130	0 863 462	09/1998	Europe				
	A131	0 696 772	02/1996	Europe				
	A132	2,752,966	03/1998	FR			See A75	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-0310US1	Application No. 10/070,091
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant Gilbert Wolrich et al.	
		Filing Date June 28, 2002	
(37 CFR §1.98(b))		Group Art Unit 2183	

### Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
	A133	WO 92/07335	04/1992	WIPO			Yes	No
	A134	WO 03/019399	03/2003	WIPO				
	A135	WO 03/085517	10/2003	WIPO				

### Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	A136	"IOP Task Switching", <i>IBM Technical Disclosure Bulletin</i> , 33(5):156-158, October 1990.
	A137	"HART, Field Communications Protocol, Application Guide", Hart Communication Foundation, pp. 1-74, (1999).
	A138	Bowden, Romilly, "What is HART?" Romilly's HART® and Fieldbus Web Site, 3 pages, (1997). <URL: <a href="http://www.romilly.co.uk/whathart.htm">http://www.romilly.co.uk/whathart.htm</a> >
	A139	Cogswell, et al., "MACS: a predictable architecture for real time systems", <i>IEEE Comp. Soc. Press, SYMP(12):296-305</i> , (1991).
	A140	Fiske et al., "Thread prioritization: a thread scheduling mechanism for multiple-context parallel processors", <i>IEEE Comput. Soc., pgs. 210-221</i> , (1995).
	A141	Hennessy et al., <i>Computer Organization and Design: The Hardware/Software Interface</i> , Morgan Kaufman Publishers, pp. 116-119, 181-182, 225-227, 447-449, 466-470, 476-482, 510-519, 712, (1998).
	A142	Heuring et al., <i>Computer Systems Design and Architecture</i> , Reading, MA, Addison Wesley Longman, Inc., pp. 174-176 and 200, (1997).
	A143	Heuring et al., <i>Computer Systems Design and Architecture</i> , Reading, MA, Addison Wesley Longman, Inc., pp. 38-40, 143-171, and 285-288, (1997).
	A144	Heuring et al., <i>Computer Systems Design and Architecture</i> , Reading, MA, Addison Wesley Longman, Inc., pp. 69-71, (1997).
	A145	Hirata, H., et al., "An Elementary Processor Architecture with Simultaneous Instruction Issuing from Multiple Threads", <i>Proc. 19th Annual International Symposium on Computer Architecture, ACM &amp; IEEE-CS</i> , 20(2):136-145, May 1992.
	A146	Intel, "IA-64 Application Developer's Architecture Guide," Rev.1.0, pp. 2-2, 2-3, 3-1, 7-165, C-4, and C-23, May 1999.
	A147	Jung, G., et al., "Flexible Register Window Structure for Multi-Tasking", <i>Proc. 24th Annual Hawaii International Conference on System Sciences</i> , vol. 1, pp. 110-116, (1991).
	A148	Kane, Gerry, <i>PA-RISC 2.0 Architecture</i> , 1995, Prentice Hall PTR, pp. 1-6, 7-13 & 7-14.
	A149	Koch, G., et al., "Breakpoints and Breakpoint Detection in Source Level Emulation", <i>Proc. 19th International Symposium on System Synthesis (ISSS '96)</i> , <i>IEEE</i> , pp. 26-31 (1996).
	A150	Moors, et al., "Cascading Content-Addressable Memories", <i>IEEE Micro</i> , 12(3):56-66 (1992).
	A151	Patterson, et al., <i>Computer Architecture A Quantitative Approach</i> , 2nd Ed., Morgan Kaufmann Publishers, Inc., pp. 447-449, (1996).

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-0310US1	Application No. 10/070,091
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant Gilbert Wolrich et al.	
		Filing Date June 28, 2002	Group Art Unit 2183

(37 CFR §1.98(b))

**Other Documents (include Author, Title, Date, and Place of Publication)**

Examiner Initial	Desig. ID	Document
	A152	Paver, et al., "Register Locking in an Asynchronous Microprocessor", <i>IEEE</i> , pp. 351-355, (1992).
	A153	Philips ED - Philips Components: "8051 Based 8 Bit Microcontrollers, Data Handbook Integrated Circuits, Book IC20", 8051 Based 8 Bit Microcontrollers, Eindhoven, Philips, NL, pp. 5-19 (1991).
	A154	Probst et al., "Programming, Compiling and Executing Partially-Ordered Instruction Streams on Scalable Shared-Memory Multiprocessors", <i>Proceedings of the 27<sup>th</sup> Annual Hawaiian International Conference on System Sciences, IEEE</i> , pp. 584-593, (1994).
	A155	Quammen, D., et al., "Flexible Register Management for Sequential Programs", <i>IEEE Proc. of the Annual International Symposium on Computer Architecture</i> , Toronto, May 27-30, 1991, vol. SYMP. 18, pp. 320-329.
	A156	Ramsey, N., "Correctness of Trap-Based Breakpoint Implementations", <i>Proc. of the 21st ACM Symposium on the Principles of Programming Languages</i> , pp. 15-24 (1994).
	A157	Sproull, R.F., et al., "The Counterflow Pipeline Processor Architecture", <i>IEEE Design &amp; Test of Computers</i> , pp. 48-59, (1994).
	A158	Steven, G.B., et al., "ALU design and processor branch architecture", <i>Microprocessing and Microprogramming</i> , 36(5):259-278, October 1993.
	A159	Young, H.C., "Code Scheduling Methods for Some Architectural Features in Pipe", <i>Microprocessing and Microprogramming</i> , Elsevier Science Publishers, Amsterdam, NL, 22(1):39-63, January 1988.
	A160	Young, H.C., "On Instruction and Data Prefetch Mechanisms", <i>International Symposium on VLSI Technology, Systems and Applications, Proc. of Technical Papers</i> , pp. 239-246, (1995).
	A161	Hidaka, Y., et al., "Multiple Threads in Cyclic Register Windows", <i>Computer Architecture News</i> , ACM, New York, NY, USA, 21(2):131-142, May 1993.
	A162	Okamoto, K., et al., "Multithread Execution Mechanisms on RICA-1 for Massively Parallel Computation", <i>IEEE - Proc. Of PACT '96</i> , pp. 116-121 (1996).

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	